



PATENT

In the United States Patent and Trademark Office

Applicant: REED et al.)
Application No: 10/630,949) Examiner: Unassigned
Filed: July 29, 2003) Art Unit: 2816
For: VOLTAGE TOLERANT CIRCUIT FOR) Date: December 19, 2003
PROTECTING AN INPUT BUFFER) Attorney Docket No.: ARTCP042

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450 on December 19, 2003.

Signed: _____


Sylvia Castillo

Separate Letter to the Official Draftsperson

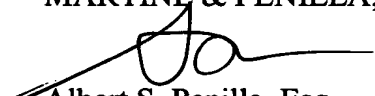
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed herewith are the seven (7) sheets of Formal Drawings (Figs. 1, 2A-2B, 3A-3B, 4, 5, 6, 7A-7B) for the above-referenced case. If the draftsperson has any questions concerning these drawings, please contact the undersigned at the number set forth below.

Applicant believes that no fees are due in connection with the filing of the Formal Drawings. However, if it is determined that any fees are due in connection with the filing of these drawings, the Commissioner is authorized to charge such fees to deposit account 50-0805 (Order No. ARTCP042).

Respectfully submitted,
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